#### SUPPLEMENTARY INFORMATION

### Orbital Tuning of Tunnel Coupling in InAs/InP Nanowire Quantum dots

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### S.1 Parameters of the device reported in the main text

Three different devices were fully characterized within this study. The first one, whose results are presented in the main text, will be called hereafter D#1, and the other two devices will be called D#2 and D#3. Figure S1, report the key experimental parameters of each of the measured devices. Experimental data was fitted using a constant interaction model and the resulting parameters are reported as a function of the filling number N, for every Coulomb blockade (CB) diamond. In particular, we report: (a) the addition energy  $\Delta U$ ; (b) the common-mode gate lever arm,  $\alpha_{cm}$ ; (c) the common-mode capacitance C<sub>cm</sub> and the total capacitance  $C_{\Sigma}$  of the quantum dot. The parameters were calculated starting from a fit of the slopes of the Coulomb diamond's edges of the stability diagram. The  $\Delta U$  values for

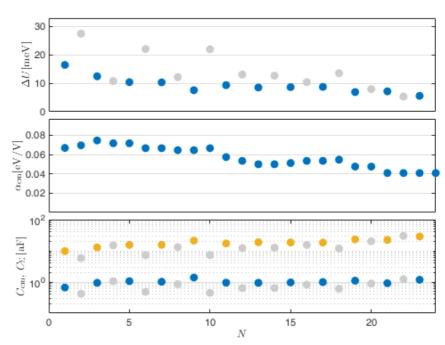


Figure S1: Main parameters of device D#1 (device reported in the main text)

odd diamonds (blue dots in the graph) correspond to the charging energies (Ec), while the energy difference between even (grey dots in the graph) and odd diamonds can be used to estimate the level spacing ( $\Delta\epsilon$ ). The charging energy in the QD of D#1 is about 10 meV, while the level spacing reaches the values up to 10-15 meV. The common-mode lever arm used for the energy conversion in Fig.5 of the main paper was obtained by a linear fit of the data reported above (around N=14).

The tunneling rate  $\Gamma$  for each peak was extracted by fitting the zero bias conductance peaks (Fig. 2.c in the main text) using the typical line-shape for a non-degenerate delta-like resonances [1]

$$G = \frac{e^2}{h} \frac{\hbar \Gamma}{k_B T} \frac{1}{4 \cosh^2 \left[ \frac{\alpha_k \left( V_g^N - V_g \right)}{2k_B T} \right]}$$

where  $\alpha_k$  is the gate lever arm,  $V_g$  is the gate voltage and  $V_g^N$  is peak gate voltage at filling N. Corrections can be expected considering the spin degeneracy, but these effects were beyond the scope of the current paper.

In Figure S2 we report the stability diagram and extracted parameters for D#2. In the plot, the back gate electrode was used to control the electron population inside the QD and an abrupt threshold to larger tunneling was observed. Devices D#2 and D#3 displayed a similar tunability using lateral gates but a full investigation of the spectral dependence could not be performed due to the presence of too many charge rearrangements over the large voltage swings necessary to perform the study.

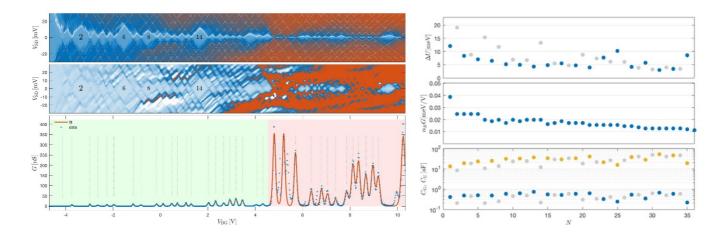


Figure S2. Left: Coulomb blockade diagram for D#2 as a function of the back gate voltage, absolute current in log scale and differential conductance at the bottom the zero-bias conductance is reported. Right: parameters extracted from the dataset. Note that the even-odd assignment is not reliable for large filling values due to the presence of charge rearrangements.

#### S.2 Numerical models

The main target of the simulations was to estimate the expected spacing between radial and axial excitations in the studied QD, i.e. the number of orbitals that need to be filled before larger tunneling resonances can be experimentally observed. To this end, we made a set of simplified assumptions and used a single particle model to predict the expected energy spacings. The nominal geometry of the studied QDs is reported in Fig.S3 the InAs island is a hexagonal box with an axial thickness  $\tau = 19.5$  nm in the z direction and a corner-to-corner diameter d = 48 nm. Calculations were performed using the PDE solver Comsol Multiphysics and the following further approximations were made: (i) the confinement potential is infinite and barrier penetration is neglected; (ii) band bending at the NW surface and the non-parabolicity effects are neglected. In these

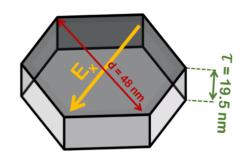


Figure S3: Sketch of the hexagonal box used as a model for the hard-wall NW QD.

approximations, the wave function can be factored as the product  $\psi(x, y, z) = A(z)B(x, y)$ , where the axial component has a particularly simple expression  $A(z) = \sin(kz)$  and  $k_z = \pi n_a/\tau$  where  $n_a$  is an integer. This leads to eigenvalues of the form

$$\varepsilon = \varepsilon_{n_r} + \varepsilon_{n_a} = \varepsilon_{n_r}(E_x) + \frac{h^2 n_a^2}{8m_e^* \tau^2}$$

where  $n_r$  is the quantum number of the radial problem and the transvers electric field acts only on  $\varepsilon_{n_r}$ . While the adopted approximations are clearly strong, the resulting estimates on the filling numbers were found to be reliable in the limit of low occupation numbers. Importantly, it has to be noted that in such a limit the transmission across the barrier only depends to the z part of the eigenvalue problem, and thus only on  $n_a$ . In particular, larger transmission amplitudes can be expected for larger  $k_z$  values and thus for larger  $n_a$  quantum numbers. This is in full agreement with the experimental observations reported in the main text in Figure 2c.

In order to allow a semi-quantitative comparison between the single-particle orbital energies reported in the main text with the applied gate voltages, a set of 2D electrostatic simulations have been performed. Figure S4 shows the results for the transverse component of the electric field  $E_x$  for the following gating

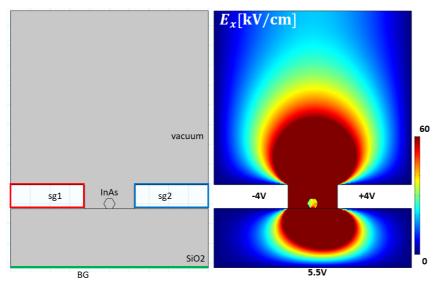


Figure S4: Finite element simulation of the electrostatics of the multi-gate architecture used for this study.

configuration:  $V_{\rm cm} = 0$ V,  $\Delta V = 8$ V and  $V_{\rm BG} = 5.5 \,\rm V$ . Known dielectric constants for silicon oxide and InAs were used to estimate the local electric field for a gate gap of 250nm (based on SEM imaging of the measured device, see Fig.S7). In the stated configuration, we obtain an average field in the x direction equal to about 40 kV/cm which leads to a consistent comparison between the energy plots reported in Fig.3 and those of Fig.5. We note the calculation does not take into account the (unknown) screening from mobile charges at the surface and in the bulk of the InAs nanowire and thus should be only regarded as a rough consistency check.

### S.3 Raw version of the colormap in Figure 5

Figure 5 in the main text shows the evolution of the Coulomb blockade peaks versus the gate imbalance  $\Delta V$ 

and the common mode voltage  $V_{\rm cm}$ . The map required significant voltage swings on the two gates, up to voltages beyond  $\pm 10 \rm V$ . Despite the general very good stability of the studied devices, charge rearrangements could not be completely avoided over such large gate swings and imperfections occurred even in the most stable devices. In order to improve the readability of the plot a reproducible charge rearrangement occurring at  $\Delta V \approx 4 \rm V$  was numerically removed in the color-plot reported in the main text. The original dataset can be seen on the right hand side on Fig.S5. Filling number are determined according the correspondence with data in Fig.2.

## S.4 Finite bias data in the anomalous crossing region

The physical origin of the anomalous crossings observed in the spectral evolution of Fig.5a, like to the one visible in Fig.5b, could not be conclusively identified in this work. Similar phenomenology has been reported in the literature and has been typically connected the role of exchange interaction in promoting a partial filling of two orbitals and a breakdown of the even-odd filling scheme. Another possibility is that direct Coulomb interaction between the two partially occupied orbitals is simply weaker, because electrons are spatially

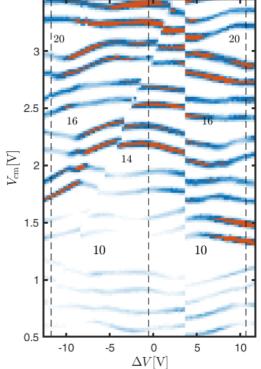


Figure S5: raw data used for the plot visible in Fig. 5 of the main text

located in two different regions of the real space. This has been convincingly demonstrated in [2], where two independent parallel dot are formed in the low filling regime, and it has also been suggested in [3], where the presence of multiple barriers can be expected to lead to a natural localization of the orbitals between different pairs of consecutive barriers. The expected origin of the effect is less obvious to identify in the current device, but one experimental possibility consists in studying the excitation spectrum and look for evidences of a splitting between triplet and single states linked to the different possible spin configurations of the two partially occupied orbitals. The current devices do not provide clear evidence of the presence of such a splitting and no conclusive result could be reached.

In Fig.S6 we report a sequence of diamond scans (differential conductance) as a function of the common-mode gate voltage  $V_{\rm cm}$ , for a discrete set of imbalance values  $\Delta V$  covering the "anomalous" crossing occurring at N=14 for  $\Delta V \approx -9$ V. Few excited state lines are visible in measurement but they appear to be connect with the partial or total filling of the available orbitals. For instance, in the sequence starting from imbalance -7V we highlighted an excitation line for the tunneling of the 14<sup>th</sup> electron. The excitation energy is found to decrease until the size of the N=14 diamond is minimized at imbalance -8.12 V. This voltage marks the beginning of the gating region where the even-odd filling scheme is violated. This excitation line is obviously related to a partial filling of both the crossing orbitals and the process becomes the lowest energy one after imbalance -8.12 V. In the case of exchange interaction, one would expect the presence of both a S=1 (lower energy) and S=0 (higher energy) resonance for such a process. According to the energy gain of 1.86meV for the partial filling of both orbitals, in the case of exchange the singlet-triplet gap would be expected to be about 3.5meV [4]. No obvious evidence for such a further resonance could be highlighted.

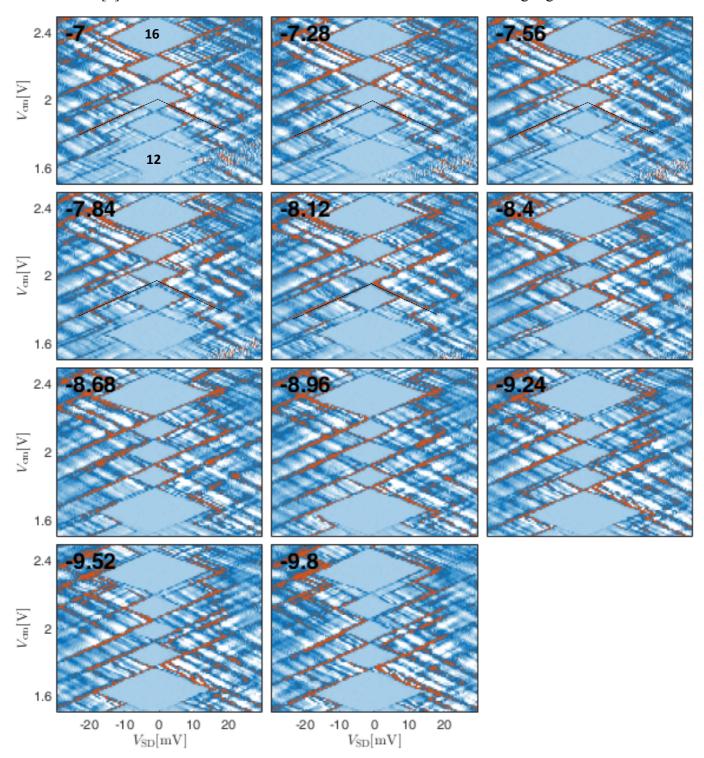


Figure S6: diamond scans obtained at different cross-sections at fixed detuning  $\Delta V$  in the filling range going from N=12 (bottom diamond) up to N=16 (top diamond)

# S.5 Device structure and measurement set-up

The structure of the studied devices is shown in Fig.S7 along with a sketch of the measurement setup. Different gaps between the gates (GG) have been studied in different devices. The device analyzed in the main text is reported in the SEM picture and had a gate gap of about 250 nm.

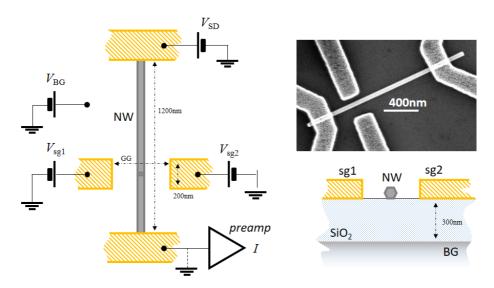


Figure S7: Device structure, SEM picture of the device studied in the main text, and sketch of the measurement setup.

#### References

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