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# Flexible Green Perovskite Light Emitting Diodes

GIUSEPPE CANTARELLA<sup>1</sup>, SUDHIR KUMAR<sup>2</sup>, CHRISTIAN VOGT<sup>3</sup> (Member, IEEE),  
STEFAN KNOBELSPIES<sup>3</sup>, ALAIN TAKABAYASHI<sup>3</sup>, JAKUB JAGIELSKI<sup>2</sup>,  
NIKO MÜNZENRIEDER<sup>4</sup> (Member, IEEE), ALWIN DAUS<sup>5</sup>, LUISA PETTI<sup>1</sup> (Member, IEEE),  
GIOVANNI A. SALVATORE<sup>3</sup>, PAOLO LUGLI<sup>1</sup> (Fellow, IEEE), CHIH-JEN SHIH<sup>2</sup>, AND GERHARD TRÖSTER<sup>3</sup>

<sup>1</sup> Sensing Technologies Laboratory, University of Bozen, 39100 Bozen, Italy

<sup>2</sup> Institute for Chemical and Bioengineering, Swiss Federal Institute of Technology, 8092 Zürich, Switzerland

<sup>3</sup> Institute for Electronics, Swiss Federal Institute of Technology, 8092 Zürich, Switzerland

<sup>4</sup> Sensor Technology Research Centre, University of Sussex, Falmer BN1 9QT, U.K.

<sup>5</sup> Department of Electrical Engineering, Stanford University, Stanford, CA 94305, USA

CORRESPONDING AUTHOR: G. CANTARELLA (e-mail: giuseppe.cantarella@unibz.it)

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**ABSTRACT** Flexible perovskite light-emitting diodes (LEDs) have attracted increasing interest to realize ultrathin, light weight, highly conformable, and nonfragile vivid displays. Solution-processed lead halide perovskite offers numerous distinctive characteristics, such as pure emission color, tunable bandgaps, and low fabrication cost. In this paper, green perovskite LEDs (PeLEDs) are fabricated on 50- $\mu\text{m}$  thick polyimide substrates. Using colloidal 2-D formamidinium lead bromide perovskite emitter, the PeLEDs show a high current efficiency ( $\eta_{\text{CE}}$ ) of 5.3  $\text{cd A}^{-1}$  with a peak emission at  $529 \pm 1$  nm and a narrow width of 22.8 nm. The resultant green emission shows color saturation  $> 95\%$ , in the Rec. 2020 standard gamut area. To demonstrate mechanical flexibility, the device functionality is tested by dynamic bending experiments down to 10 mm for up to 5000 cycles, resulting in device lifetime over 36 h in a glove box and a drop of  $\eta_{\text{CE}}$  and external quantum efficiency ( $\eta_{\text{ext}}$ ) as low as 15% and 18%, respectively. For the selective activation of multiple PeLEDs,  $7 \times 7$  passive arrays on rigid and flexible substrates are demonstrated. Moreover, preliminary results of active matrices show the compatibility of PeLEDs with oxide-based thin-film transistors (TFTs) for display applications.

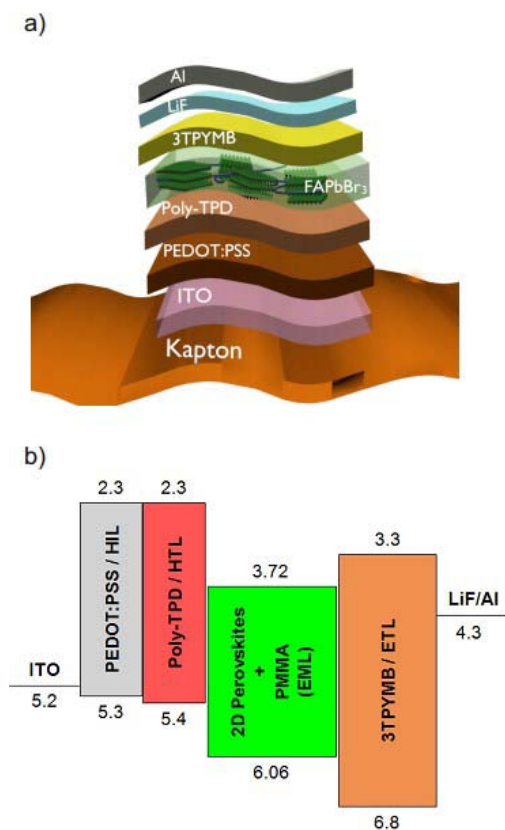
**INDEX TERMS** Perovskite, light-emitting diodes (LEDs), flexible, bending, pixel array, passive matrix, active matrix.

## I. INTRODUCTION

The field of flexible electronics has attracted substantial interest for new and challenging applications, which require electrical functionality on curvilinear and complex surfaces. Together with wearable electronics [1]–[3] and health monitoring systems [4], flexible displays are considered as most promising candidates for light-weight, ultra-thin, portable, and nonfragile consumer electronics [5]. This new class of displays calls for high performances, high energy efficiency and long-operational stability, as well as low fabrication cost for mass production. In the past years, numerous types of emitters, such as, organic emitters and chalcogenide quantum dots, have been extensively investigated in light emitting diodes to achieve efficient and color pure flexible displays [6], [7]. However, the conventional

technologies were not able to realize Rec. 2020 standard green emission because of their broad spectral width and a bathochromic shift in photoluminescence after forming the thin solid films. Recently, colloidal organic-inorganic hybrid perovskites (OIHPs) have emerged as the most promising color-pure emitters in light emitting diodes (LEDs) because of their cost-effective facile synthesis at low temperature and easy scalability. Moreover, they show very narrow emission width, tunable bandgap, and high photoluminescence quantum yield (PLQY) [8], [9]. Although few flexible green perovskite LEDs (PeLEDs) have been demonstrated, color-pure electroluminescence that shows a 95% color saturation in Rec. 2020 standard gamut, has never been achieved by far.

In this work, we demonstrate color-pure green flexible PeLEDs using colloidal two dimensional (2D)

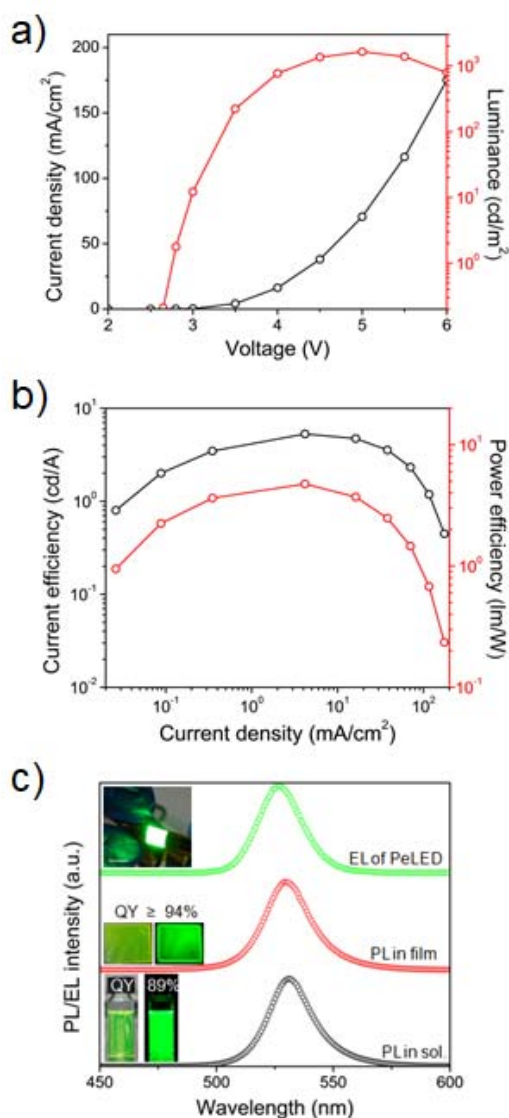


**FIGURE 1.** Perovskite-based LED (PeLED) on flexible substrate. a) Schematic of the device layer stack and b) energy level diagram of materials utilized (data unit: eV).

formamidinium lead bromide (FAPbBr<sub>3</sub>) perovskite emitters. The 2D FAPbBr<sub>3</sub> shows a solid-state PLQY as high as 94%. The devices, fabricated on 50  $\mu\text{m}$  thick polyimide foil using a spin-casted emission layer, show a high current efficiency ( $\eta_{\text{CE}}$ ) of 5.3  $\text{cd A}^{-1}$  and a color saturation of  $\sim 96\%$ . Moreover, the device exhibits a peak emission at 529 nm. Differently from [9], to systematically test the device flexibility under mechanical strain, PeLEDs are evaluated in dynamic bending experiments, showing functionality in a nitrogen-filled glove box down to 10 mm bending radius for up to 5000 bending cycles. Moreover, the fabrication scalability allows the realization of passive and active multipixel matrices, consisting of 49 PeLEDs, on both rigid and flexible substrates.

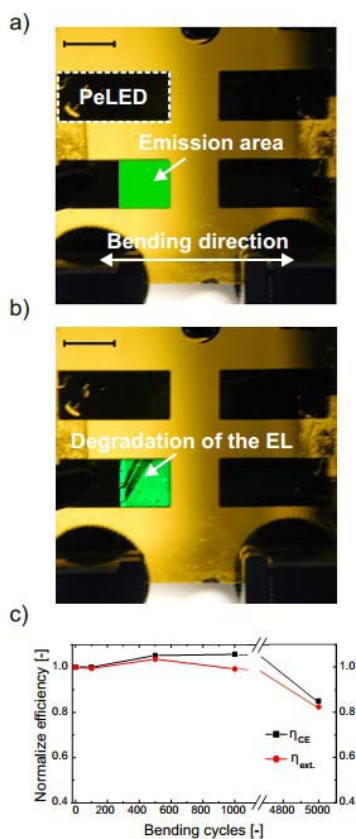
## II. MATERIALS AND DEVICE FABRICATION

Monodisperse FAPbBr<sub>3</sub> perovskites are synthesized by using the synthetic route reported in [10]. The resultant nanocrystals (NCs) show a PLQYs of  $\sim 89\%$  and  $94\%$  in solution and spin-casted thin film, respectively. Moreover, the EL emission locate at 531 nm in solution and 529 nm in spin-coated film. The unprecedented higher PLQY in the thin film sample is a result of aggregation induced emission [11]. A schematic architecture of flexible PeLEDs is shown in Fig. 1a. A 50  $\mu\text{m}$ -thick polyimide (Kapton)



**FIGURE 2.** Flexible PeLED based on 2D FAPbBr<sub>3</sub>. a) Current density and luminance as a function of voltage. b) Current efficiency and power efficiency as a function of current density. c) Electroluminescence (EL) spectrum at 4 V and photoluminescence (PL) spectra, showing the ultra-pure green emission at  $529 \pm 1$  nm. Inset: photograph of flexible PeLED glowing at 4V with a bending radius of 2.5 mm, perovskite thin film on glass substrate (left panel in ambient light and right panel under ultraviolet irradiation), and colloidal solutions of perovskites under ambient light (left panel) and ultraviolet light (right panel).

foil (from DuPont) is utilized as the substrate. First, this substrate is cleaned in acetone and isopropanol for 10 min and cured in oven for 48 h at 200  $^{\circ}\text{C}$ , to remove any solvent residuals. To ensure a high mechanical and chemical stability during the device fabrication, a 50 nm SiN<sub>x</sub>, acting as buffer layer, is deposited by PECVD on both sides. Once the Kapton foil preparation is complete, a 120 nm thick ITO anode layer is deposited through RF sputtering in Ar atmosphere. Then, the desirable ITO pattern is obtained through UV photolithography and lift-off process. Subsequently, the patterned ITO substrate is exposed to

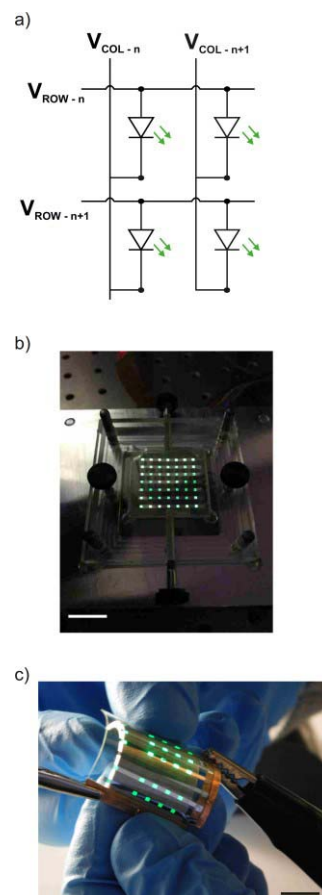


**FIGURE 3.** PeLEDs under dynamic bending experiments. a) Optical picture for a) a fresh emitting device (0.5 h lifetime) and b) after 5000 cycles (lifetime > 36 h) (Scale bar: 5 mm). The formation of black lines in the emitting area prove the Perovskite degradation in ambient condition. c) Normalized  $\eta_{CE}$  and  $\eta_{ext}$  for a PeLED bent down to 10 mm bending radius for 100, 500, 1000 and 5000 cycles.

oxygen plasma for 10 min to ensure a smooth ITO surface without any contamination. Thereafter, a hole injection layer,  $32 \pm 3$  nm PEDOT:PSS, is deposited by spin coating. Then, a hole transporting layer (HTL),  $18 \pm 2$  nm poly[*N,N'*-bis(4-butylphenyl)-*N,N'*-bis(phenyl)-benzidine] (Poly-TPD), is spin-coated in a glovebox. Before spin-coating, colloidal FAPbBr<sub>3</sub> NCs are mixed with the low-*k* PMMA host. The resultant emissive layer ( $30 \pm 5$  nm) is then deposited onto the Poly-TPD layer. Then, the flexible substrate is transferred to a high vacuum chamber and a 45 nm electron transporting layer, tris(2,4,6-trimethyl-3-(phenyl)borane (3TPYMB), a 1 nm LiF electron injection layer, and a 70 nm Al cathode layer, are sequentially evaporated with the deposition rates of 0.5, 0.1, and  $\sim 2$  Å s<sup>-1</sup>, respectively. Particularly, the LiF and Al layers are deposited through a shadow mask. The active-area of device is defined as the overlap between ITO and Al layers. Fig. 1b shows the energy band diagram of the materials involved in the device stack.

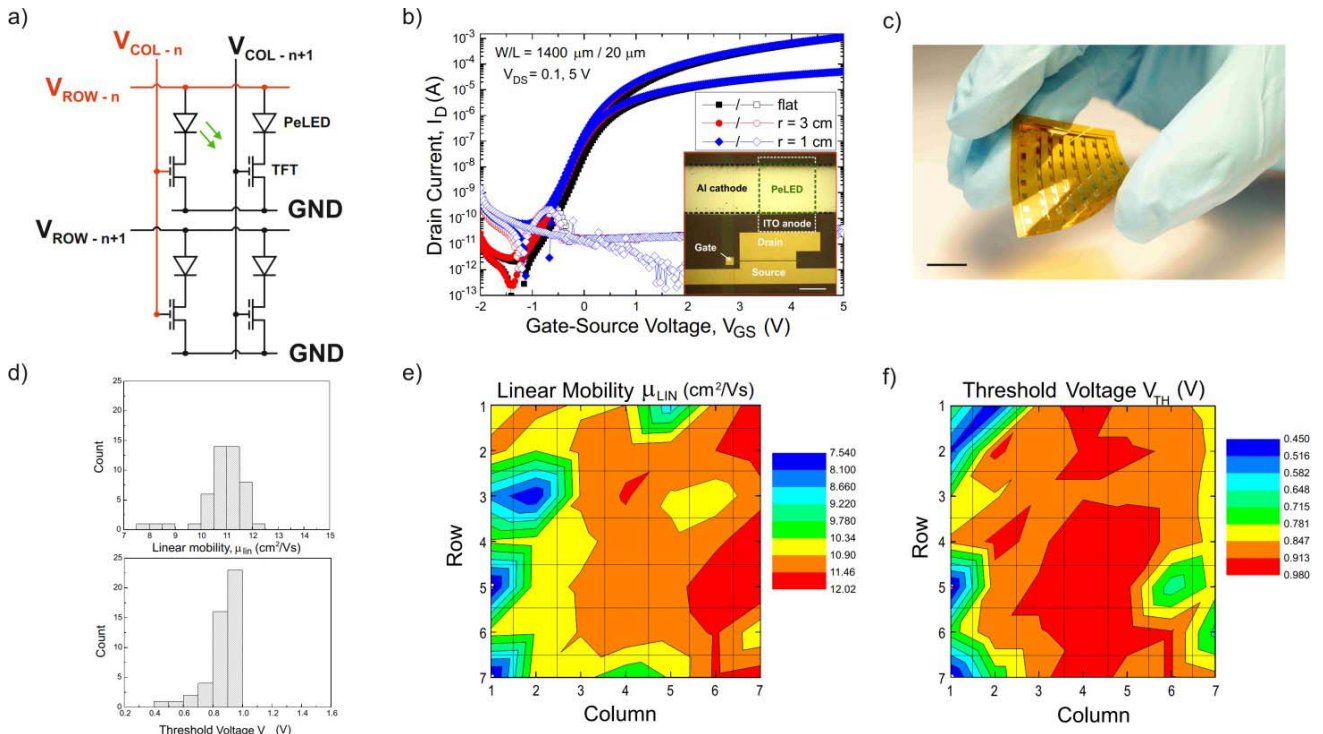
### III. PELEDs ELECTRICAL CHARACTERIZATION

The current density (*J*) and luminance (*L*) of a champion device as a function of applied voltage (*V*) is presented in Fig. 2a. The device shows a low turn-on voltage of 2.8 V



**FIGURE 4.** PeLED passive matrix. a) Layout of a passive matrix, where the selection of a column line ( $V_{col-n}$ ) and a row line ( $V_{row-n}$ ) allows the PeLED activation. An array of 7 x 7 PeLEDs is realized on both b) a rigid glass slide (characterized by using a custom made setup) and c) on a PET flexible substrate (where Cu tape is used for improving the electrical contact). For both substrates, the PeLED area is 1 mm x 2 mm (Scale bar: 1 cm) and the driving voltage is 5 V.

and a maximum luminance of  $1642 \text{ cd m}^{-2}$ . Moreover, our optimized device demonstrate a high  $\eta_{CE}$  of  $5.30 \text{ cd A}^{-1}$  and a power efficiency ( $\eta_{PE}$ ) of  $4.76 \text{ lm W}^{-1}$  with a high external quantum efficiency ( $\eta_{ext}$ ) of 1.29% (see Fig. 2b). As shown in Fig. 2c, PeLEDs exhibit a peak electroluminescence ( $\lambda_{EL}$ ) at  $529 \pm 1 \text{ nm}$  and a full width at half maximum of 22.8 nm with Commission internationale de l'éclairage *x* and *y* coordinates ( $CIE_{x,y}$ ) of (0.173, 0.768). The  $CIE_{x,y}$  color-coordinates presented here show a color saturation of  $\sim 96\%$  in the 1931 CIE color space in Rec. 2020 standard gamut area, which has never been achieved in the flexible LEDs so far. We attribute the high device performance to a charge injection balance and efficient recombination of injected carriers under electrical excitation. Moreover, the charge transporting layers, Poly-TPD and 3TPYMB, possessed an efficient carrier (electron and hole) confinement function. Moreover, our devices also exhibit a consistent EL spectrum with the PL spectra of 2D FAPbBr<sub>3</sub> perovskites (Fig. 2c). To the best of our understanding, we present the first flexible PeLED using precise layer controlled colloidal 2D perovskites.



**FIGURE 5.** Schematic and performance of the IGZO based active matrix. **a)** The green PeLED functionality is activated when the corresponding TFT is on-state (namely, its column and row lines are selected). The operating voltage for  $V_{COL}$  and  $V_{ROW}$  is 8 V. **b)** IGZO-based TFT characteristics ( $W/L = 1400 \mu\text{m}/20 \mu\text{m}$ ) in flat and bent conditions. For each IGZO TFT, a PeLED is fabricated in series (inset) (Scale bar: 500  $\mu\text{m}$ ). **c)** The 7 x 7 active matrix is fabricated on a free-standing 50  $\mu\text{m}$  thick kapton foil (Scale bar: 1 cm). The electrical performance of the matrix is evaluated. **d)** Histogram of the linear mobility  $\mu_{lin}$  and threshold voltage  $V_{th}$  for the array. Here, a yield of  $\approx 96\%$  is achieved. **e-f)** Coloured 2D mapping of the same parameters over the substrate area (3 cm x 3 cm).

#### IV. MECHANICAL BENDING EXPERIMENTS

To test the device performance under mechanical stress, a dynamic bending experiment is executed. The flexible substrate, with four PeLEDs (for each device, the emission area is equal to 5 mm x 5 mm) (see Fig. 3a), is mounted on a dynamic bending setup, and bent down to 10 mm bending radius. In this test, the PeLEDs are bent for a certain number of times (100, 500, 1000 and 5000 cycles), and then electrically characterized in ambient condition (see Fig. 3a and 3b), monitoring the  $\eta_{CE}$  and  $\eta_{ext}$ . At zero cycles, the PeLEDs show a  $\eta_{CE}$  and a  $\eta_{ext}$  of 3.47 cd A<sup>-1</sup> and 0.85%, respectively, at an operational voltage of 3V. Unexpectedly, slightly higher efficiencies are observed after 500 cycles that can be attributed to light induced healing in the perovskite layer. After 5000 cycles, corresponding to > 36 h, the device efficiencies exhibit a drop of 15 % in the  $\eta_{CE}$  and 18 % in the  $\eta_{ext}$  (see Fig. 3c). In general, the lowering of the device performance is mainly attributed to the absence of a passivation layer. Indeed, the interaction with ambient moisture (occurring during the characterization in ambient condition) and the emission layer degradation, is proved by the formation of black lines in the PeLED emitting area (see Fig. 3b). Indeed, future studies need to understand the degradation processes of Perovskites and find possible solutions, to increase device lifetime and broaden the applications of this class of materials [12].

#### V. PASSIVE MATRIX

The ease of fabrication scalability is demonstrated with the realization of an array of 49 PeLEDs (arranged in 7 lines and 7 columns). The passive matrix consists in a scheme where each PeLED (or pixel) is activated without any driving circuitry (i.e., capacitors, Thin-Film Transistors (TFTs), etc.). In this way, a pixel is turned on when both its corresponding column line ( $V_{COL-n}$ ) and row line ( $V_{ROW-n}$ ) are selected (see Fig. 4a). To prove the fabrication compatibility with flat planes as well as flexible displays, passive PeLED matrices are realized on both glass and Polyethylene terephthalate (PET) substrates, commercially available from Sigma-Aldrich (substrate area = 3 cm x 3 cm). The choice of changing the carriers is due to an higher quality of the ITO layer, acting as anode, with the respect to the sputtered one. Fig. 4b shows the rigid matrix in a custom made setup for the selective characterization of the pixels. In parallel, the flexible array (Fig. 4c) is tested while bending strain is applied, showing functionality down to 1 cm bending radius. A stable EL emission is observed throughout the bending test. Most interestingly, the devices demonstrate stable EL peak at 529 nm during the mechanical strain test.

#### VI. TOWARDS ACTIVE MATRIX

To prove the compatibility of PeLEDs with other electronics, the next step is their integration with Thin-Film

Transistors (TFT), to form an active matrix. For this purpose, metal-oxide semiconductors, and in particular amorphous Indium-Gallium-Zinc-Oxide (a-IGZO), have been widely used for flexible electronics [13]–[15], due to the low fabrication complexity, large-area deposition and field-effect mobility as high as  $10 \text{ cm}^2/\text{Vs}$ . The active matrix display is designed with an array of  $7 \times 7$  driving cells, where an IGZO TFT drives in series a PeLED, as presented in Fig. 5a. Each single TFT is selected by choosing the corresponding column line  $V_{\text{COL}}$  and row line  $V_{\text{ROW}}$ , allowing the PeLED functionality (see Fig. 5a). Considering the J-V curve for the PeLEDs (see Fig. 2a), the W/L ratio for the TFTs is chosen to provide enough drain current  $I_D$ . For a LED area of  $2 \text{ mm} \times 1 \text{ mm}$  and a voltage across the LED of  $8.5 \text{ V}$ ,  $I_D$  has to be equal to  $2 \text{ mA}$  (reached by a  $W/L = 1400 \text{ }\mu\text{m}/20 \text{ }\mu\text{m}$ ) (see Fig. 5b). With these design parameters, the TFTs are fabricated on a flexible  $50 \text{ }\mu\text{m}$ -thick Polyimide (or kapton) foil using a bottom-gate inverted staggered configuration (see Fig. 5c). Similarly to other works [3], a  $120 \text{ nm}$  ITO layer is RF sputtered in Ar atmosphere at room temperature, and patterned as the gate contact for the TFT, and anode pad for the PeLED, using a lift-off process in acetone and isopropanol. Afterwards, aluminum oxide ( $\text{Al}_2\text{O}_3$ ), IGZO and Ti/Au are deposited as gate dielectric (and passivation layer), semiconductor and source/drain contacts, respectively [16], [17]. To conclude the array fabrication, the TFTs are protected with photoresist (AZ1518, from MicroChemicals) to allow the cleaning of the ITO pads in the PeLED stack by an  $\text{O}_2$  plasma treatment, without damaging the TFTs. Once the step is performed, acetone and isopropanol are used to remove the photoresist. Next, the PeLEDs fabrication is performed as presented in paragraph II. An optical picture of the flexible display is presented in Fig. 5c.

The overall matrix uniformity, in terms of electrical properties, is evaluated. The fabrication yield is as high as 96%, with two TFTs (over the whole matrix) not properly functional on the substrate edge, due to damages in the gate dielectric layer, resulting in high leakage current ( $I_G > 1 \text{ mA}$ ). An average linear mobility  $\mu_{\text{lin}}$ , threshold voltage  $V_{\text{TH}}$ , subthreshold swing SS and  $I_{\text{ON}}/I_{\text{OFF}}$  ratio of  $10.88 \text{ cm}^2/\text{Vs}$ ,  $0.86 \text{ V}$ ,  $130 \text{ mV/dec}$  and  $3.9 \times 10^9$ , are extracted. Figure 5d gives an overview of the variation of linear mobility and threshold voltage, while a colored 2D mapping of the same parameters is presented in Fig. 5e and 5f.

Here, additional optimizations are required to improve the active matrix performance, in terms of LED brightness, as well as uniform activation of each pixel. Nevertheless, these preliminary results show the compatibility of oxide-based Thin-Film electronics with Pervoskites, for display applications.

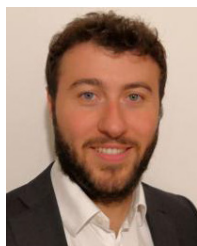
## VII. CONCLUSION

In this work, flexible PeLEDs are presented. A low-cost and room temperature manufacturing process is developed to

allow fabrication compatibility with flexible substrates. The green PeLEDs, realized on a  $50 \text{ }\mu\text{m}$  kapton foil with  $2\text{D}$   $\text{FAPbBr}_3$  nanocrystals, show peak emission at  $529 \text{ nm}$ , turn-on voltage  $V_{\text{on}}$  of  $2.8 \text{ V}$  and a maximum current efficiency  $\eta_{\text{CE}}$  of  $5.30 \text{ cd A}^{-1}$ . To prove the device flexibility, dynamic bending experiments are performed, showing functionality while bent down to  $10 \text{ mm}$  bending radius for 5000 cycles. Moreover, passive matrixes as well as active arrays are realized on both rigid and flexible substrates, for the selective activation of multiple PeLEDs. Given the compatibility with standard IGZO TFT technology (room temperature and low-cost deposition), colloidal  $2\text{D}$   $\text{FAPbBr}_3$  perovskites could represent a potential contestant for next-generation flexible display devices.

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**GIUSEPPE CANTARELLA** received the M.Sc. degree in micro and nanotechnology for ICTs from the Polytechnic of Turin, Italy, in 2013 and the Ph.D. degree in electrical engineering from ETH Zürich, Zürich, Switzerland, in 2018. He is currently a Post-Doctoral Fellow with the Free University of Bozen-Bolzano, Bolzano, Italy, and ETH Zürich, where he researches on flexible and printed electronics.



**JAKUB JAGIELSKI** received the B.Sc. degree in chemical engineering and the M.Sc. degree in chemical- and bioengineering from ETH Zürich, Switzerland, in 2014 and 2016, respectively, where he is currently pursuing the Ph.D. degree with the Lab for Interface and Surface Engineering of Nanomaterials under the supervision of Prof. C.-J. Shih. He was an Intern, CSIRO, Australia, on the development of MOFs as heavy metal absorbers. His research activities focus on the synthesis of luminescent, 2-D perovskite nanoplatelets for the fabrication of efficient and color-pure LEDs.



research mainly focuses on the design and fabrication of organic LEDs and perovskite LEDs for next-generation displays and human-friendly illumination devices.

**SUDHIR KUMAR** received the Ph.D. degree in materials science and engineering from National Tsing Hua University, Taiwan, in 2014, where he was a Post-Doctoral Fellow. In 2015, he joined ETH Zürich, where he is currently a Post-Doctoral Fellow with the Prof. Shih Group, aiming at the development of energy-efficient and ultraviolet pure light emitting diodes (LEDs) based on colloidal 2-D perovskite nanocrystals and a Senior Scientist with the Lab for Interface and Surface Engineering of Nanomaterials. His



**NIKO MÜNZENRIEDER** (S'11–M'14) received the Diploma degree in physics from Technische Universität München, Munich, Germany, in 2008 and the Ph.D. degree in electrical engineering from ETH Zürich, Zürich, Switzerland, in 2013. He is currently a Senior Lecturer leading the Flexible Electronics Laboratory, University of Sussex, Brighton, U.K., where he is researching on flexible oxide electronics, smart textiles, and thin-film sensor systems.



**CHRISTIAN VOGT** (S'13–M'17) received the M.Sc. degree in electrical engineering and information technology and the Ph.D. degree in electrical engineering from the Swiss Federal Institute of Technology Zürich, Zürich, Switzerland, in 2013 and 2017, respectively. His research interests include flexible electronics and applications for magnetic resonance imaging.



**ALWIN DAUS** received the M.Sc. degree in electrical engineering from the Braunschweig University of Technology, Braunschweig, Germany, in 2013 and the Ph.D. degree in electrical engineering and information technology from ETH Zürich, Zürich, Switzerland, in 2018. He is currently a Post-Doctoral Scholar with Stanford University, Stanford, USA. His current research interest includes flexible electronics with 2-D materials.



**STEFAN KNOBELSPIES** received the B.Sc. and M.Sc. degrees in microsystems engineering from the Albert Ludwigs University of Freiburg, Freiburg im Breisgau, Germany, in 2011 and 2015, respectively, and the Ph.D. degree from the Institute for Electronics, ETH Zürich, Zürich, Switzerland, in 2018. Since 2018, he has been with Sensirion.



**LUISA PETTI** (S'12–M'16) received the M.Sc. degree in electronic engineering from the Politecnico di Milano, Milan, Italy, in 2011 and the Ph.D. degree in electrical engineering and information technology from ETH Zürich, Zürich, Switzerland, in 2016. She is currently an Assistant Professor with the Free University of Bozen-Bolzano, Bolzano, Italy, where she researches on flexible and printed electronics.



**ALAIN TAKABAYASHI** received the M.Sc. degree in electrical engineering from the Swiss Federal Institute of Technology Zürich, Zürich, Switzerland, in 2017. He is currently pursuing the Ph.D. degree in photonic micro- and nanosystems with the Quack Group, Swiss Federal Institute of Technology Lausanne, Lausanne, Switzerland. His current research focuses on integrating MEMS with an established silicon photonics platform for the implementation of zero-power reconfigurable photonic integrated circuits.



**GIOVANNI A. SALVATORE** received the bachelor's degree in electronics and the master's degree in micro and nanotechnology from the Polytechnic of Turin in 2004 and 2006, respectively, and the Ph.D. degree from EPFL for his research on ferroelectric transistor (EPFL these 4990) in 2011. He was a Post-Doctoral Fellow with Electronics Laboratory, ETH Zürich. He is currently a Scientist with the ABB Corporate Research Center, Baden, involved in the packaging and reliability of electric devices.



**PAOLO LUGLI** (SM'07–F'11) received the Graduation degree in physics from the University of Modena, Modena, Italy, in 1979 and the M.Sc. and Ph.D. degrees in electrical engineering from Colorado State University, Fort Collins, CO, USA, in 1982 and 1985, respectively. He is currently a President with the Free University Bozen-Bolzano, Bolzano, Italy. He has authored over 350 scientific papers.



**GERHARD TRÖSTER** was a Full Professor of electronics with the Institute of Electronics, ETH Zürich from 1993 to 2018, where he headed the Digital Systems and Wearable Computing Lab. His research interests include wearable and mobile computing platforms, sensor fusion, machine learning, flexible electronics, thin-film technology, and smart textile aiming at applications in healthcare, sports, and music.



**CHIH-JEN SHIH** received the Ph.D. degree from the Massachusetts Institute of Technology in 2014. He has been a Tenure-Track Assistant Professor of chemical engineering with the Department of Chemistry and Applied Biosciences, ETH Zürich since 2015. He was a Post-Doctoral Fellow with Stanford University from 2014 to 2015. His research group focuses on morphology, dynamics, molecular forces, and transport phenomena at nanomaterials interfaces. His interests range from fundamental understanding of how dielec-

tric screening of atomically thin nanomaterials influences the movement and interactions of charges, excitons, and molecules near interfaces, to application-motivated studies aimed at developing new engineering strategies to control over the interplay of these mechanisms, toward new technological opportunities in optoelectronics, sensors, and actuators. He was a recipient of the Victor K. LaMer Award from the American Chemical Society and the Ruzicka Prize from the Swiss Chemical Society.