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# Quenching the band gap of 2D semiconductors with a perpendicular electric field

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The electronic band structure of atomically thin semiconductors can be tuned by the application of a perpendicular electric field. The principle was demonstrated experimentally shortly after the discovery of graphene by opening a finite band gap in graphene bilayers[1–3], which naturally are zero-gap semiconductors. So far, however, the same principle could not be employed to control a broader class of materials, because the required electric fields are beyond reach in current devices. To overcome this limitation, we have realized double ionic gated transistors that enable the application of very large electric fields. Using these devices, we show that the band gap of few-layer semiconducting transition metal dichalcogenides can be continuously suppressed from 1.5 eV to zero. Our results illustrate an unprecedented level of control of the band structures of 2D semiconductors, which is important for future research and applications.

An electric field applied perpendicular to the surface of a bulk semiconductor is screened over a finite length, leaving the material interior unaffected. In atomically thin semiconductors [4], however, the small thickness prevents efficient screening, so that a perpendicular electric field uniformly influences the entire system, modifying its band structure[5–11]. Indeed, a zero-gap semiconductor such as bilayer graphene can be turned into a gapped insulator using double-gated transistors to apply a perpendicular electric field[1–3]. Despite representing a true conceptual breakthrough, continuous control of the band structure in transistors has not found widespread use, because the limited maximum electric field that can be applied in common devices does not allow significant changes to be induced in most 2D materials.

Here we demonstrate the ability to fully quench band gaps as large as 1.5 eV using a new type of transistors based on double ionic gates. Ionic gating relies on electrolytes to transfer the potential from a metallic gate to the surface of a semiconductor[12–14]. In our devices, two electrolytes in contact with two independent gate electrodes are coupled to a same atomically thin semiconductor (see Fig. 1A). The top electrolyte is a commonly used ionic liquid[12–16], and the bottom one is a Li-ion glass ceramic substrate[17, 18]. The atomically thin semiconductor is also connected to two metal electrodes functioning as source and drain contacts, and surrounded by a ground plane (an Aluminum film sandwiched between two  $\text{Al}_2\text{O}_3$  layers) to eliminate any direct electrostatic coupling of the top and bottom electrolyte (See Supplementary Note S1 for details on device fabrication and S3 for the decoupling of the

gates). The use of single ionic gate devices to accumulate charge densities unattainable with conventional gating is by now established[12, 14, 19–23]. However, double ionic gate devices to control independently accumulated charge and electric field have never been reported earlier.

When grounding one of the gates and sweeping the voltage applied to the other, these double gate devices function as conventional transistors, as illustrated by the transfer curves ( $I_{SD}$  measured as a function of gate voltage) recorded on a representative WSe<sub>2</sub> tetralayer (4L) device (see Fig. 1B and 1C; for additional characterization measurements, see Supplementary Note S2). Simultaneously applying a positive voltage  $V_{IL}$  to the ionic liquid gate and a negative voltage  $V_{BG}$  to the back gate also enables a perpendicular electric field to be established with no net charge accumulated on the semiconductor. To investigate the effect of the perpendicular electric field on the band structure, we look at how the transfer curves  $I_{SD}$ -vs- $V_{IL}$  of our device evolve for increasing negative  $V_{BG}$  values (Fig. 2A).

For  $V_{BG} = -0.8$  V, the device transfer curve is qualitatively identical to that measured for  $V_{BG} = 0$  V (Fig. 1C): the current  $I_{SD}$  increases as  $V_{IL}$  is swept past the threshold for electron accumulation ( $V_{IL} = 1.7$  V), and no current flows for  $V_{IL} < 1.7$  V, when the chemical potential is in the gap. At  $V_{BG} \sim -1$  V, however, the transfer curve exhibits clear qualitative differences, as the current flows even for  $V_{IL}$  well below 1.7 V. As  $V_{BG}$  is further increased to more negative values, the source-drain current  $I_{SD}$  remains large for all values of  $V_{IL}$ , without ever vanishing (with a square resistance  $R_{sq} \sim 10$  k $\Omega$  for all  $V_{IL}$ ). Analogous considerations hold true when looking at the evolution of the  $I_{SD}$ -vs- $V_{BG}$  transfer curve upon applying a positive voltage  $V_{IL}$  to the ionic liquid gate.

The complete evolution of  $I_{SD}$  as a function of  $V_{IL}$  and  $V_{BG}$  is illustrated by the color plot in Fig. 2C. The observed behavior is not the one expected if the only effect of the gate voltages was to affect the electrostatic potential in the transistor channel, i.e., the mechanism that determines the operation of a conventional transistor. In that case, both  $V_{IL}$  and  $V_{BG}$  would just shift the energy of the 4L-WSe<sub>2</sub> bands, and a change in the voltage applied to one of the gates would only cause a rigid shift in the transfer curve measured as a function of the voltage applied to the other gate. The data, however, do not show a rigid shift. That is because the concomitant application of large positive  $V_{IL}$  and large negative  $V_{BG}$  also generates a perpendicular electric field that quenches the band gap, in agreement with previous theoretical calculations[5, 9] and our own (see Fig. 1D and 1E and Supplementary

Note S7).

To understand, we look at the evolution of transport as  $V_{IL}$  and  $V_{BG}$  are varied continuously along the contour outlined by the colored line in Fig. 2C. At point A,  $V_{IL} = 0V$  and  $V_{BG} = -2.5 V$ . The negative potential  $V_{BG}$  results in the accumulation of holes (see Fig. 1B), and sets the chemical potential in the WSe<sub>2</sub> valence band (the square resistance is  $R_{sq} \sim 8 \text{ k}\Omega$ , Fig. 2B). As  $V_{IL}$  is increased from 0 to 2.5 V at fixed  $V_{BG} = -2.5 V$ , we travel from points A to B (green line). Since the capacitance of the ionic liquid and of the Li-ion glass gates are the same to a very good approximation, the electrostatic potential applied to the channel is proportional to  $V^* = V_{IL} + V_{BG}$ , and goes back to zero at B. Nevertheless, the current remains large. If then  $V_{BG}$  is decreased from -2.5 to 0 V at fixed  $V_{IL} = 2.5 V$  we move from B to C (purple line), where transport is mediated by electrons accumulated by the large positive voltage  $V_{IL}$  (see Fig. 1C). Transport therefore evolves from being mediated by holes at point A (with the chemical potential in the valence band), to being mediated by electrons at point C (with the chemical potential in the conduction band), without ever passing through a highly insulating state (see Fig. 2D). This is possible only if the gap closes and the conduction and valence band overlap in some part of the contour, with electrons and holes coexisting in the transistor channel. Indeed, this happens in the neighborhood of B, where the electric field perpendicular to the 4L-WSe<sub>2</sub>—proportional to  $E^* = (V_{IL} - V_{BG})$ —is maximum.

For a quantitative analysis, we plot the current  $I_{SD}$  as a function of  $V^* = V_{IL} + V_{BG}$  and  $E^* = V_{IL} - V_{BG}$  (Fig. 3A). Fig. 3B shows  $I_{SD}$ -vs- $V^*$  curves measured at fixed values of  $E^*$ . At small  $E^*$ ,  $I_{SD}$  is finite for sufficiently large negative and positive  $V^*$ —with current mediated by holes and electrons respectively—and vanishes over an extended  $V^*$  interval, as the chemical potential in 4L-WSe<sub>2</sub> is swept across the gap. This interval shrinks as  $E^*$  increases, and for  $E^* > E_c^*$ , no highly resistive state is observed, indicating that under these conditions no gap is present. To determine  $E_c^*$ , we extract the threshold voltages for electron and hole conduction ( $V_{T-e}^*$  and  $V_{T-h}^*$ ), plot the difference  $\delta = V_{T-e}^* - V_{T-h}^*$  as a function of  $E^*$ , and look at when  $\delta$  vanishes (Fig. 3C). The corresponding electric field is then approximately given by  $E_c^*/t_{TMD} \simeq 1.1 \text{ V/nm}$ , where  $t_{TMD} = 2.6 \text{ nm}$  is the thickness of 4L-WSe<sub>2</sub>. This is a slight overestimate, because part of the voltage drops across the screening length of the two electrolytes. However, as we discuss in detail in Supplementary Note S5, the very large capacitance of the gate electrolytes ( $C \approx 50 \mu\text{F/cm}^2$

[12, 13, 15, 17, 24]) ensures that the voltage drop across the electrolytes is small, only approximately 10 %, from which we conclude that the electric field  $\mathcal{E}_c$  needed to quench the gap of 4L WSe<sub>2</sub> is between 0.9 and 1.0 V/nm (note that this is the actual electric field, and not the displacement field, which is frequently referred to in the literature when analyzing experiments on double-gated transistors). We have performed similar experiments also on 3L, 5L and 7L WSe<sub>2</sub> and succeeded in closing the band gap in all cases (see Supplementary Note S6 for data measured on devices of different thickness). The required electric  $\mathcal{E}_c$  fields are plotted in Fig. 3D.

The evolution of the  $I_{SD}$ -vs- $V_{SD}$  curves upon increasing the perpendicular electric field confirms that the gap closes (see Fig. 4). At  $E^* = 0$ , a pronounced suppression of  $I_{SD}$  over a broad  $V_{SD}$  range is observed when the 4L-WSe<sub>2</sub> device is biased at  $V^* = V_{IL} + V_{BG} = 0.2$  V, corresponding –for  $V_{SD} = 0$  V– to setting the chemical potential well inside the gap. Upon increasing  $E^*$  (with  $V^*$  fixed), the  $I_{SD}$  suppression becomes less pronounced and eventually the  $I_{SD}$ -vs- $V_{SD}$  curve becomes perfectly linear. That is what happens after the gap has closed, when the overlap of valence and conduction band is sufficiently large.

Within the precision of both theory and experiments, the value of electric field needed to quench the gap of few-layer WSe<sub>2</sub> is in agreement with expectations (See Supplementary Note S7). In qualitative terms, the gap closes when the electrostatic potential is sufficient to lift the energy of the valence band edge at one crystal surface above the conduction band edge at the opposite surface, so that the conduction and valence band overlap, and the system becomes gapless. This conclusion holds true for all semiconductors, but the nature of the gapless state depends on details. For sufficiently thin crystals –such as 4L-WSe<sub>2</sub>– the electron and hole wavefunctions at opposite surfaces are expected to overlap in space and hybridize. In this regime, the appearance of topological states has been predicted in semiconducting transition metal dichalcogenides[25–27]. For thicker crystals hybridization eventually becomes negligible, and the electron and hole accumulation layers on opposite surfaces only couple through Coulomb interaction. Closing the gap of atomically thin semiconductors with a perpendicular electric field therefore leads to the formation of electron-hole systems that have not been realized earlier.

Tuning the gap continuously –and suppressing it altogether– discloses the possibility to control virtually all aspects of the physics of a semiconductor, including its emission and absorption spectrum, as well as other optical and transport properties. As such, the

electrostatic control of the band gap opens new routes for the realization of technologically relevant photonic devices. Examples are broadly tunable light sources[28], possibly covering energy ranges in which achieving light emission is traditionally difficult (e.g., in the very far infrared), or optical modulators. We therefore anticipate that the results presented here will attract interest in the fields of optoelectronics and optical communications.

### **DATA AVAILABILITY**

The data that support the findings of this study are available from the corresponding author without any restriction.

### **AUTHOR CONTRIBUTIONS**

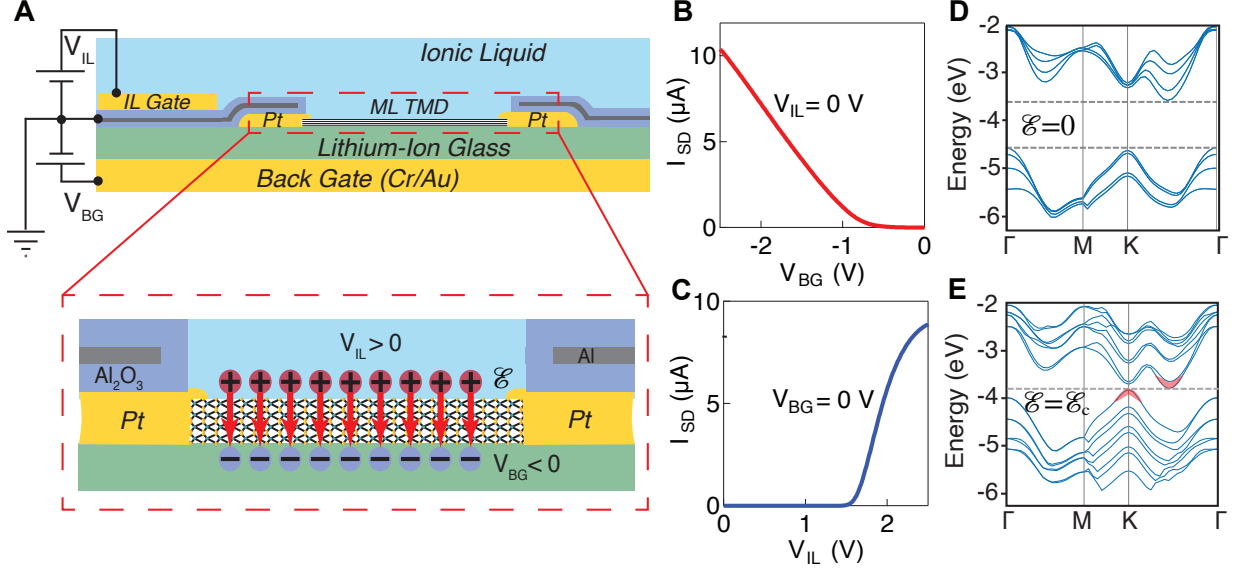
D.D., M.P., N.U., and I.G.L. fabricated the devices and carried out the experiments. D.D, M.P., I.G.L, and A.F.M. analysed the experimental data. M.G. performed first-principles simulations. I.G.L. supervised the experimental work and A.F.M. conceived and directed the research. All authors participated in preparing the manuscript.

### **ACKNOWLEDGEMENTS**

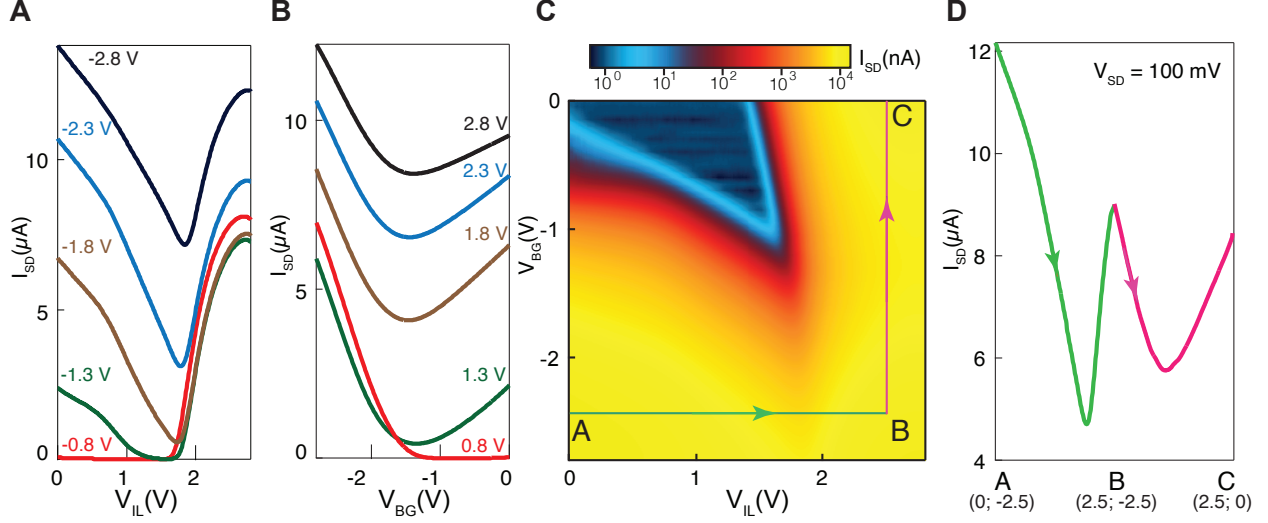
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### **COMPETING INTERESTS**

The authors declare no competing interests.



**FIG. 1. Double ionic gated field-effect transistors.** **A** (top panel) Schematic cross-section of a multilayer WSe<sub>2</sub> transistor equipped with top (ionic liquid) and bottom (Li-ion conductive glass ceramic) electrolyte gates. Also shown are the Platinum contacts to the TMD multilayer and a Al<sub>2</sub>O<sub>3</sub>/Al/Al<sub>2</sub>O<sub>3</sub> trilayer to decouple electrostatically the top and bottom electrolytes. (bottom panel) Zoom in on the device channel area (not to scale). When the two gates are biased with opposite polarity the accumulated charges compensate, and a uniform perpendicular electric field  $\mathcal{E}$  is established across the TMD (represented by the red arrows in the scheme). **B** Source-drain current  $I_{SD}$  measured for negative voltages  $V_{BG}$  applied to the Li-ion glass gate (with the ionic liquid gate grounded,  $V_{IL} = 0$  V), resulting in the accumulation of holes in WSe<sub>2</sub>. **C**  $I_{SD}$  measured as a function of  $V_{IL} > 0$  V for  $V_{BG} = 0$  V, to cause electron accumulation in the transistor (the applied source-drain voltage is  $V_{SD} = 0.1$  V). Note that the application of positive  $V_{IL}$  and negative  $V_{BG} < 0$  V causes the Li ions to be pulled away from the TMD, ensuring that intercalation does not take place. **D** and **E** represent the band structure of 4L WSe<sub>2</sub> computed within density-functional theory –respectively for zero perpendicular electric  $\mathcal{E} = 0$  V/nm, and at the critical field  $\mathcal{E} = \mathcal{E}_c$ – and show that quenching of the gap with an electric field is expected theoretically (see Supplementary Note S7 for details; in **(E)**, the conduction and valence band edge at  $\mathcal{E}_c$  are denoted by red shaded lines).



**FIG. 2. Electrical characteristics of a double-gated 4L-WSe<sub>2</sub> transistor.** **A** Source-drain current  $I_{SD}$  as a function of ionic liquid gate voltage  $V_{IL}$ , for different negative values of back gate voltage  $V_{BG}$ . The curves evolve from exhibiting textbook transistor behavior at small negative  $V_{BG}$  (see also Fig. 1B), to not showing any sizable current suppression at large negative  $V_{BG}$ . **B** Same as **A**, with the source-drain current  $I_{SD}$  measured as a function of  $V_{BG}$  for different positive values of  $V_{IL}$ ; the evolution of the transistor curves is fully analogous to the one shown in **A**. **C** Color plot of  $I_{SD}$  (in logarithmic scale) as a function of  $V_{BG}$  and  $V_{IL}$ . Note that the simultaneous application of a large negative  $V_{BG}$  and of an equally large positive  $V_{IL}$  causes the current in the transistor to increase by 4-to-5 orders of magnitude, despite leaving the potential of the transistor channel ( $V^* = V_{IL} + V_{BG}$ ) unchanged. **D** Evolution of  $I_{SD}$  along the A-B-C contour illustrated in panel **(D)** (the coordinates of A, B, and C in the  $(V_{IL}, V_{BG})$  plane are indicated at the bottom). Transport in the transistor is mediated by holes at A and by electrons at C: finding that nowhere the current is fully suppressed implies that on part of the A-B-C contour the valence and conduction band of WSe<sub>2</sub> must overlap (i.e., the band gap closes). In all measurements  $V_{SD} = 0.1$  V.

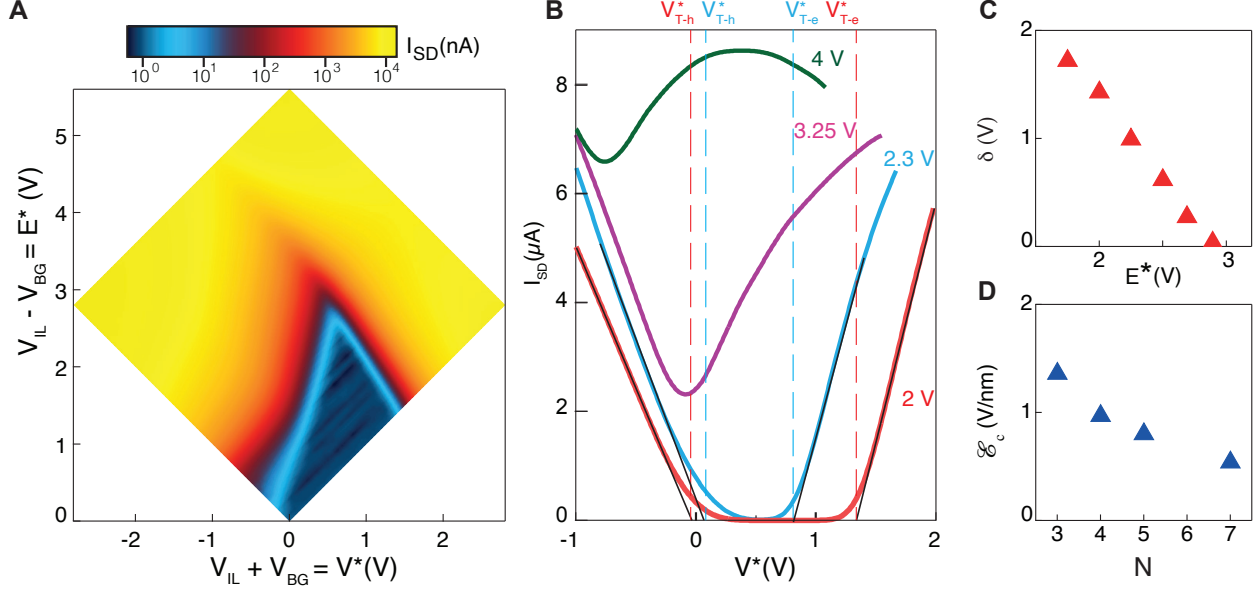


FIG. 3. **Band gap evolution as a function of electric field.** **A** Colour plot of the source-drain current  $I_{SD}$  (in logarithmic scale) as a function of  $V^* = V_{IL} + V_{BG}$ , and  $E^* = V_{IL} - V_{BG}$  (respectively proportional to the electrostatic potential in the transistor channel and to the electric field perpendicular to the 4L-WSe<sub>2</sub> crystal). The width of the  $V^*$  interval over which the current  $I_{SD}$  is suppressed decreases monotonically upon increasing  $E^*$ . **B** For a quantitative analysis, we look at horizontal cuts of the color plot in (A),  $I_{SD}$ -vs- $V^*$  at fixed  $E^*$  (values indicated in the figure). The curves measured at  $E^* = 2.0$  and 2.3 V exhibit a complete suppression of  $I_{SD}$ , and the black thin lines illustrate how we determine the threshold voltage for electron ( $V_{T-e}^*$ ) and hole ( $V_{T-h}^*$ ) conduction (by extrapolating  $I_{SD}$  to zero for positive and negative values of  $V^*$ ; the position of the threshold voltages are marked by the vertical dashed lines). **C** We plot  $\delta = V_{T-e}^* - V_{T-h}^*$  as a function of  $E^*$ , and find the value  $E_c^*$  for which  $\delta = 0$  V ( $E_c^* = 2.9$  V in our 4L-WSe<sub>2</sub> device) to determine the condition at which the gap closes. **D** The voltage difference  $E_c^*$  measured for WSe<sub>2</sub> crystals with  $N = 3, 4, 5, 7$  layers is converted into an electric field  $\mathcal{E}$ , by modeling our device as a stack of three dielectrics, corresponding to the double layers of the two electrolyte gates and the WSe<sub>2</sub> multilayer (the very large, known capacitance of the electrical double layers makes the procedure rather insensitive to the value of the dielectric constant of the TMD; see Supplementary Note S5 for details). Experimental data on 3L, 5L and 7L WSe<sub>2</sub> devices is presented in Supplementary Note S6.

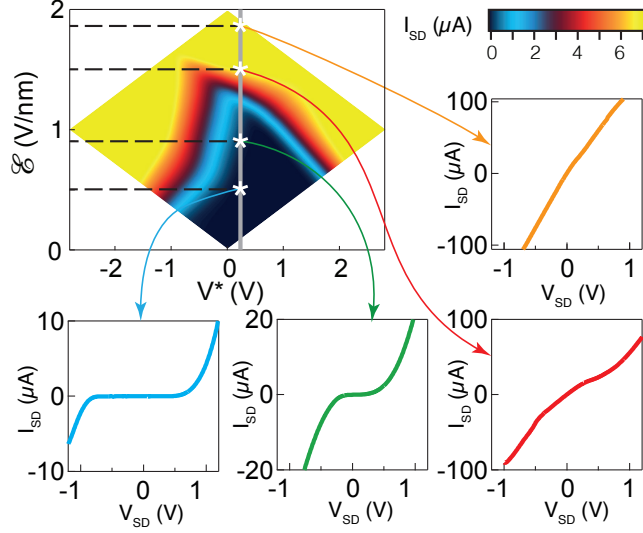


FIG. 4. **Evolution of the  $I_{SD}$ -vs- $V_{SD}$  curves.** The top left panel is a linear-scale color plot of  $I_{SD}$  as a function of  $V^* = V_{IL} + V_{BG}$  and of perpendicular electric field  $\mathcal{E}$ . The other surrounding panels –with curves of different color– show the  $I_{SD}$ -vs- $V_{SD}$  characteristics of our transistor measured at  $V^* = 0.2$  V for different values of  $\mathcal{E}$  (indicated by the white starts in the color plot). At  $\mathcal{E} = 0.5$  V/nm (blue curve) a very strong non-linearity with a large suppression of  $I_{SD}$  at small  $V_{SD}$  is clearly visible, as expected (because  $V^* = 0.2$  V corresponds to having the chemical potential inside the band gap of 4L-WSe<sub>2</sub>). Upon increasing the perpendicular electric field, the non-linearity in the  $I_{SD}$ -vs- $V_{SD}$  characteristics becomes less pronounced (green curve,  $\mathcal{E} = 0.87$  V/nm). Increasing the perpendicular electric field further fully quenches the gap, causing the suppression of  $I_{SD}$  at low  $V_{SD}$  to become negligible (red curve,  $\mathcal{E} = 1.5$  V/nm), and eventually resulting in a virtually perfectly linear  $I_{SD}$ -vs- $V_{SD}$  curve (orange curve,  $\mathcal{E} = 1.9$  V/nm).

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